Figure 1 Global Control Signals, Inputs and Outputs at Specified Bits
Figure 2 Writing and then Reading a “1” at Block 15 WL63 Bit42
Figure 3 Writing and then Reading a "0" at Block 15 WL63 Bit44
Figure 4 Writing and then Reading a “1” at Block 15 WL0 Bit42
Figure 5 Writing and then Reading a "0" at Block 15 WL0 Bit44
Figure 6 Layout of a Single Bitcell
Figure 7 Layout of 4-by-4 Bitcells